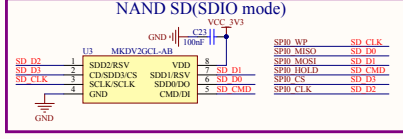
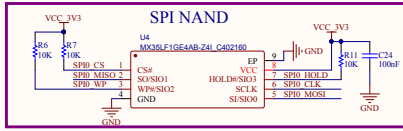
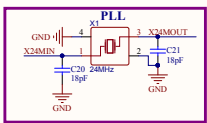
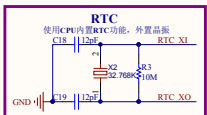
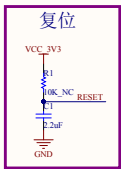
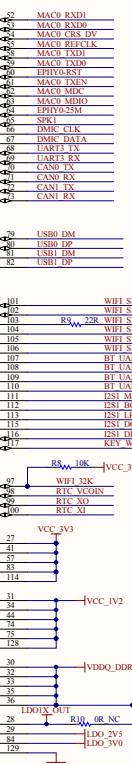
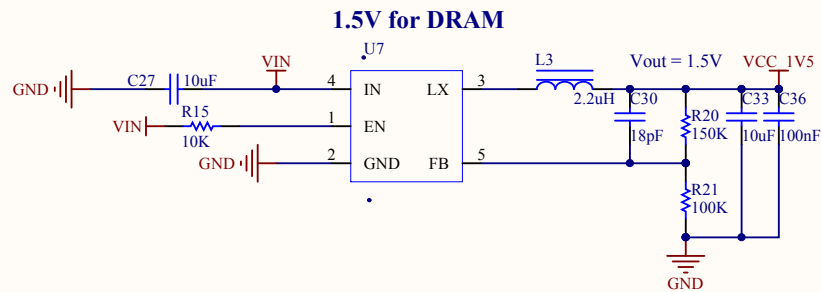
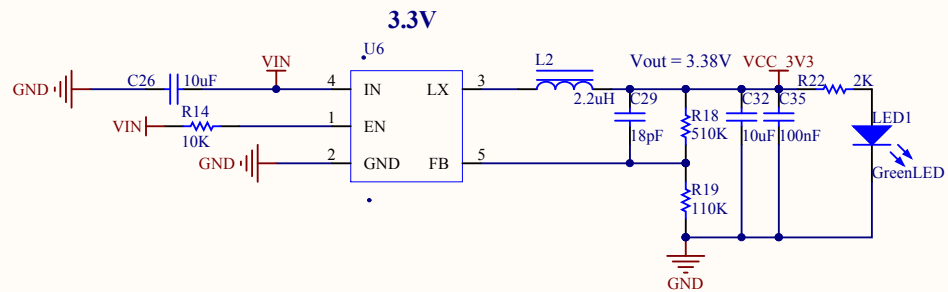
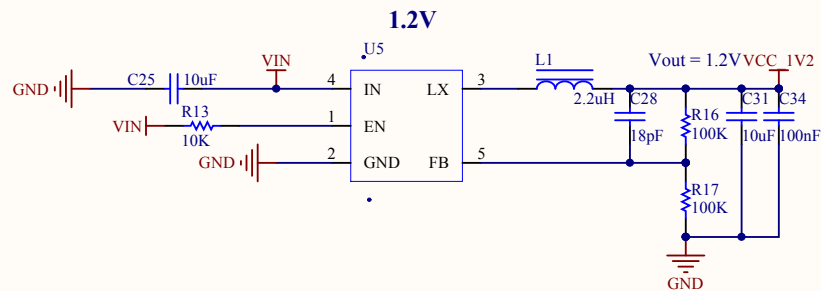


UART0_TX	85	GPA_P0GPA0PSADC12C2_SCL_UART0_TX0M
UART0_RX	86	GPA_P1GPA1PSADC12C2_SDA_UART0_RX0M
USBD_ID	87	GPA_P2GPA2PSADC12C2_UART0_RTS
LC3_SLEEP	88	GPA_P3GPA3PSADC12C2_UART0_CTS
UART1_TX	89	GPA_P4GPA4PSADC12C2_UART1_TX
UART1_RX	90	GPA_P5GPA5PSADC12C2_UART1_RX
KEY_AK3	91	GPA_P6GPA6PSADC12C2_SDA_UART1_RTS
PA_SIDD	92	GPA_P7GPA7PSADC12C2_SDA_UART1_CTS
RTP_XP_RST	93	GPA_P8RTP_XPPSADC12C2_SCL_UART1_TX1
RTP_XP_INT	94	GPA_P9RTP_XPPSADC12C2_SDA_UART1_RX1
RTP_XN_SDA	95	GPA_P10RTP_XNPSADC12C2_SCL_UART1_RX1
RTP_XN_SDA	96	GPA_P11RTP_XNPSADC12C2_SDA_UART1_C1
SP0_HOLD	17	GPE_P0SDCO_CMDSP0_HOLD12C1_SCL_UART7
SP0_WP	18	GPE_P1SDCO_D3SP0_WP12C1_SDA_UART7_R
SP0_CS	19	GPE_P2SDCO_D3SP0_CS
SP0_MISO	20	GPE_P3SDCO_D0SP0_MISO
SP0_MOSI	21	GPE_P4SDCO_D1SP0_MOSI
SP0_CLK	22	GPE_P5SDCO_D2SP0_CLK
UART4_TX	23	GPE_P6SDCO_D4SP1_HOLD12C2_SCL_UART4
UART4_RX	24	GPE_P7SDCO_D5SP1_WP12C2_SDA_UART4_RX
UART5_TX	25	GPE_P8SDCO_D6SP1_CSI_UART4_RTS_UART5_T
UART5_RX	26	GPE_P9SDCO_D7SP1_MISO_UART6_RTS_UART5
UART6_TX	27	GPE_P10SDCO_D8SP1_MOSI_UART6_TX
UART6_RX	28	GPE_P11SDCO_R8SP1_CLK_UART6_RX
SDC1_D1	19	GPC_P0SDC1_D1LCD_D8SP2_CLK_UART1_TX
SDC1_D0	20	GPC_P1SDC1_D0LCD_D4SP2_CSI_UART1_RX1
SDC1_CLK	21	GPC_P2SDC1_CLKLCD_D3SP2_MOSI_UART1_R
SDC1_CMD	22	GPC_P3SDC1_CMDLCD_D2SP2_MISO_UART1_T
SDC1_D3	23	GPC_P4SDC1_D3LCD_D1_UART2_RX1_UART0_RX
SDC1_D2	24	GPC_P5SDC1_D2LCD_D0_UART2_RTS_UART1_TX
SDC1_DET	25	GPC_P6SDC1_DETCLK_OUT0_RTU_UART1_RX
SDC1_PWM	26	GPC_P7_UART1_RTS_PWM3_B
BUZZER	18	GPD_P0LCD_D0SP2_CLK_PBUS_A00PWM0_A
WiFi Power ON	17	GPD_P1LCD_D1SP2_CS_PBUS_A00PWM0_B
WiFi Host Wake	16	GPD_P2LCD_D2SP2_MOSI_TE_PBUS_A00
BT Power ON	15	GPD_P3LCD_D3SP2_CLK_PBUS_A00PWM1_B
BT Host Wake	14	GPD_P4LCD_D4SP2_CS_PBUS_A00PWM2_A
BT Wake	13	GPD_P5LCD_D5SP2_MOSI_TE_PBUS_A00PWM2_B
LC0_SCL	12	GPD_P6LCD_D6SP1_MISO12C0_SCL_UART1_T
LC0_SDA	11	GPD_P7LCD_D7SP1_MISO12C0_SDA_UART1_R
LVDS1_D0N	10	GPD_P8LCD_D8LVDS1_D0NSP1_HOLD_UART2
LVDS1_D0P	9	GPD_P9LCD_D9LVDS1_D0PSP1_WP_UART2_RX
LVDS1_D1N	8	GPD_P10LCD_D10LVDS1_D1NSP1_CSI_UART3
LVDS1_D1P	7	GPD_P11LCD_D11LVDS1_D1PSP1_MISO_UART
LVDS1_D2N	6	GPD_P12LCD_D12LVDS1_D2NSP1_MISO_UART
LVDS1_D2P	5	GPD_P13LCD_D13LVDS1_D2PSP1_CLK_UART4
LVDS1_CKN	4	GPD_P14LCD_D14LVDS1_CKNSP1_CLK_CAP0
LVDS1_CKP	3	GPD_P15LCD_D15LVDS1_CKNSP1_CSN_CAP0
LVDS1_D3N	2	GPD_P16LCD_D16LVDS1_D3NSP1_MOSI_CAP2
LVDS1_D3P	1	GPD_P17LCD_D17LVDS1_D3PSP1_MISO_A00M
MPI_DSI_D0	12	GPD_P18LCD_D18LVDS0_D0NDSI_D0N12C1_S
MPI_DSI_D1	11	GPD_P19LCD_D19LVDS0_D0PDSI_D0P12C1_S
MPI_DSI_D2	10	GPD_P20LCD_D20LVDS0_D1NSI_D1N1_UART1
MPI_DSI_D3	9	GPD_P21LCD_D21LVDS0_D1PDSI_D1P1_UART1
MPI_DSI_CK-	8	GPD_P22LCD_D22LVDS0_D0NDSI_CKN12C1_S
MPI_DSI_CK+	7	GPD_P23LCD_D23LVDS0_D2PDSI_CKP12C1_S
MPI_DSI_D4	6	GPD_P24LCD_D24LVDS0_CKNDSI_DEN_UART3
MPI_DSI_D5	5	GPD_P25LCD_D25LVDS0_CKNDSI_D2P_UART3_R
MPI_DSI_D6	4	GPD_P26LCD_VS1LVDS0_D1NDSI_D1N_PWM3_A
MPI_DSI_D6+	3	GPD_P27LCD_D26LVDS0_D3PDSI_D3P_PWM3_B
RESET	26	SYS_RSTN
X34MIN	27	PLL_X0
X34MOUT	28	PLL_X1
NC(NEED_FLOATING)	29	
NC(NEED_FLOATING)	30	

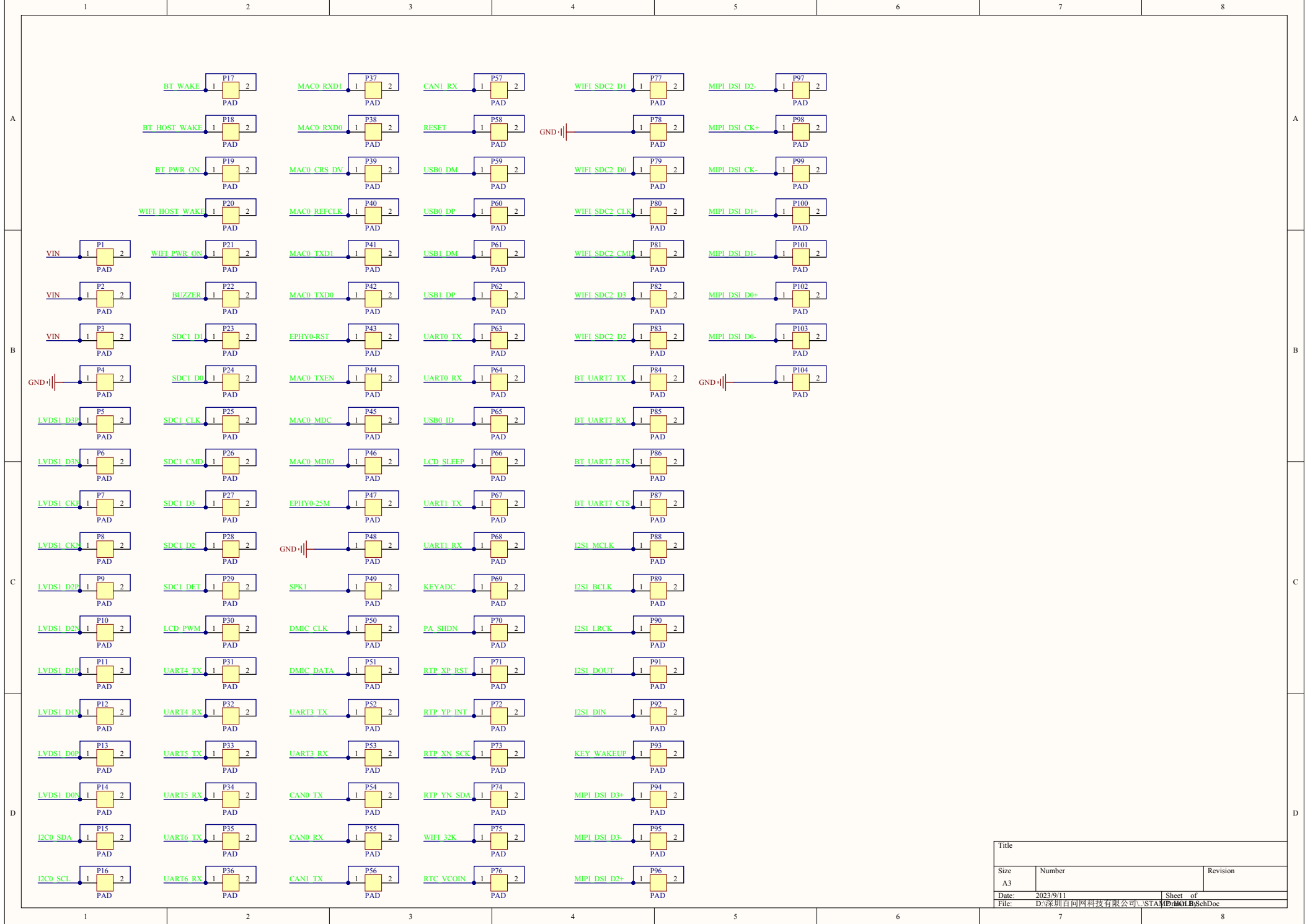
GPE_P0DVP_D0I2C0_SCLGMACO_RXD0PWM0_A	52	MAC0_RXD1
GPE_P1DVP_D1I2C0_SDAGMACO_RXD0PWM0_B	53	MAC0_RXD0
GPE_P2DVP_D2CAN0_TX1UART4_TXGMACO_RXC	54	MAC0_CRS_DIV
GPE_P3DVP_D3CAN0_RX1UART4_RXGMACO_CLK	55	MAC0_BE1CLK
GPE_P4DVP_D4CAN1_TX1UART5_TXGMACO_TXD	56	MAC0_TXD1
GPE_P5DVP_D5CAN1_RX1UART5_RXGMACO_TXD	57	MAC0_TXD0
GPE_P6DVP_D6CAN1_RX1UART5_RXGMACO_TXD	58	EPHY0RST
GPE_P7DVP_D7CAN1_RX1UART5_RXGMACO_TXD	59	MAC0_TXEN
GPE_P8DVP_D8CAN1_RX1UART5_RXGMACO_TXD	60	MAC0_MCR
GPE_P9DVP_D9CAN1_RX1UART5_RXGMACO_TXD	61	MAC0_MER0
GPE_P10DVP_D10CAN1_RX1UART5_RXGMACO_TXD	62	EPHY0SM
GPE_P11DVP_D11CAN1_RX1UART5_RXGMACO_TXD	63	SPK1
GPE_P12DVP_D12CAN1_RX1UART5_RXGMACO_TXD	64	DMC_CLK
GPE_P13DVP_D13CAN1_RX1UART5_RXGMACO_TXD	65	DMC_DATA
GPE_P14DVP_D14CAN1_RX1UART5_RXGMACO_TXD	66	UART3_TX
GPE_P15DVP_D15CAN1_RX1UART5_RXGMACO_TXD	67	UART3_RX
GPE_P16DVP_D16CAN1_RX1UART5_RXGMACO_TXD	68	CAN0_TX
GPE_P17DVP_D17CAN1_RX1UART5_RXGMACO_TXD	69	CAN0_RX
GPE_P18DVP_D18CAN1_RX1UART5_RXGMACO_TXD	70	CAN1_TX
GPE_P19DVP_D19CAN1_RX1UART5_RXGMACO_TXD	71	CAN1_RX
P01USBD_DM1UART0_RX1UART1_RX	79	USBD_DM
P01USBD_DP1UART0_TX1UART1_TX	80	USBD_DP
P02USBD_DM1UART0_RX1UART2_RX	81	USBD_DM
P02USBD_DP1UART0_TX1UART2_TX	82	USBD_DP
GPE_P0SDC2_D1SP2_CLK_UART5_TXGMACO1_R	101	WiFi_SDC2_D1
GPE_P1SDC2_D0SP2_CSI_UART5_RXGMACO1_RX	102	WiFi_SDC2_D0
GPE_P2SDC2_CLK_SP2_MOSI_UART5_RTS_GMAC	103	WiFi_SDC2_CLK
GPE_P3SDC2_CMD_SP2_MISO_UART5_CTS_GMAC	104	WiFi_SDC2_CMD
GPE_P4SDC2_D3_UART6_TXGMACO1_TXD1_PBUS	105	WiFi_SDC2_D3
GPE_P5SDC2_D2_UART6_RXGMACO1_TXD0_PBUS	106	WiFi_SDC2_D2
GPE_P6_UART7_TXGMACO1_TXD0_PBUS_A00	107	BT_UART7_TX
GPE_P7_UART7_RXGMACO1_TXD1_PBUS_A00	108	BT_UART7_RX
GPE_P8_UART7_CSI_GMAC1_M00_PBUS_A00	109	BT_UART7_CTS
GPE_P9_UART7_CSI_GMAC1_M00_PBUS_A00	110	BT_UART7_CTS
GPE_P10I2S1_MCLK_I2S1_D0N_UART1_C1S_CLK	111	I2S1_MCLK
GPE_P11I2S1_BCLK_PBUS_A00_UART1_TX_GMA	112	I2S1_BCLK
GPE_P12I2S1_LRCLK_UART1_RTS_UART3_RX_GMA	113	I2S1_LRCLK
GPE_P13I2S1_DOUT_I2S1_D0N_UART1_C1S_UART	114	I2S1_DOUT
GPE_P14I2S1_DIN_D0N_SPK0DMIC_D0_UART4_TX	115	I2S1_DIN
GPE_P15I2S1_TE_D0N_SPK0DMIC_CLK_UART4_RX_GM	116	I2S1_WAKEUP
RTC_IO	87	WiFi_32K
VCON_RTC	88	RTC_VCON
RTC_X0	89	RTC_X0
RTC_X1	90	RTC_X1
VCC3_V0	27	
VCC3_V1	31	
VCC3_V2	33	
VCC3_V3	34	
VCC3_V4	35	
VCC3_V5	36	
VDD1_SVS1	31	
VDD1_SVS2	32	
VDD1_SVS3	33	
VDD1_SVS4	34	
VDD1_SVS5	35	
VDD1_SVS6	36	
VDDQ_D0R1	30	
VDDQ_D0R2	31	
VDDQ_D0R3	32	
VDDQ_D0R4	33	
VDDQ_D0R5	34	
LDO1X_OUT	28	
LDO2X_OUT	29	
LDO3X_OUT	34	
VCC30_ANA	129	
GND		



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